

Appl. No. 10/711,181
Amdt. dated November 21, 2005
Reply to Office action of September 01, 2005

REMARKS/ARGUMENTS

1. Rejection of claims 1-6 under 35 U.S.C. 102(b):

Claims 1-6 are rejected under 35 U.S.C. 102(b) as being anticipated by MacInnis (US 6,573,905).

Response:

Independent claims 1 and 12 have been amended to overcome this rejection. Claims 1 and 12 now specify that the first clock generator provides "the same clock having the same frequency and the same phase to the video capture engine and the video display engine so as to synchronize the signal receiving rates of the video capture engine and the video display engine." That is, the same exact clock is provided to both the video capture engine and the video display engine. Claims 5, 8, 16, and 19 have also been amended in the same way as claims 1 and 12. No new matter is added through these amendments to the claims.

On the other hand, MacInnis teaches a way of synchronizing two clocks, but does not teach that the same clock with the same frequency and phase should be input to both the video capture engine and the video display engine. In column 11, lines 59-67, MacInnis teaches that the video input received through decoder 50 is input at a nominal rate of 13.5 MHz, which is synchronized with the display clock also having a nominal rate of 13.5 MHz. However, in col.12, lines 1-4, MacInnis teaches that these two clocks are not necessarily the same frequency, and can also be offset from each other.

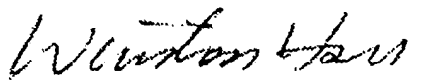
MacInnis uses the phrase "the clock runs **nominally** at 13.5 MHz", and also states that the two clocks are not necessarily the same frequency and are preferably offset from each other. The word "nominally" implies that the clocks have an approximate frequency of 13.5 MHz. Therefore, MacInnis does not teach that the video capture clock should be the same clock used for video display.

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Thus, since MacInnis does not teach that the two clocks are in phase and have the same frequency, MacInnis does not anticipate all of the claim limitations contained in claims 1 and 12. MacInnis only teaches the rough synchronization of two clocks, and does not teach that the same clock with the same frequency and phase are input to the video capture engine and the video display engine. Claims 2-11 and 13-22 are dependent on claims 1 and 12, and should be allowed if claims 1 and 12 are allowed. Reconsideration of claims 1-22 is therefore respectfully requested.

Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

Sincerely yours,



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